

In the Abstract:

On new page 38 following the claims, please insert the following abstract:

METHOD AND MEMORY CONTROLLER FOR SCALABLE MULTI-CHANNEL
MEMORY ACCESS

Abstract of the Disclosure

An electrical device is connected to at least one memory accessing unit and to a memory including at least one physical memory module. The device includes at least one access channel circuit connected to the least one memory accessing unit via at least one system bus and to the at least one physical memory module. The access channel circuit provides memory access for the at least one memory accessing unit to at least a part of the memory.